

**BACK PRESSURE TEST FIXTURE TO ALLOW PROBING OF  
INTEGRATED CIRCUIT PACKAGE SIGNALS**

**ABSTRACT OF THE DISCLOSURE**

A test fixture is disclosed for applying force to the surface of a printed circuit board to maintain proper electrical contact between the printed circuit board and an IC package or other electrical component on the opposite side of the board. The fixture employs a link structure to apply force to the board surface while still allowing access to contact points of interest on the side of the board opposite the IC package.

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